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approximate analysis stack distance

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C Ding, Y Zhong - ACM SIGPLAN Notices, 2003 - portal.acm.org

... attainable through three techniques: **approximate** analysis ... originally called LRU **stack distance**), pattern recognition ... discusses possible uses of this **analysis**. ...

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[PS] **[Reuse distance analysis for scientific programs](#)** - group of 4 »

Y Zhong, C Ding, K Kennedy - Proceedings of Workshop on Languages, Compilers, and Run- ..., 2002 - cogsci.rochester.edu

... access trace [3] or stores all program data in a **stack** [10] or ... incurs a tiny fraction of the memory at the cost of accurate **analysis**. ... **approximate** trace, which ...

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[Efficient \(Stack\) Algorithms for Analysis of Write-Back and Sector Memories](#) - group of 4 »

JG THOMPSON, AJAY SMITH - ACM Transactions on Computer Systems, 1989 - portal.acm.org

... The level at which the block is found is referred to as its stuck **distance**; see Figure 1. Using **stack analysis**, it is possible to compute the miss ratio of ...

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[Estimating cache misses and locality using stack distances](#) - group of 6 »

C CaBcaval, DA Padua - Proceedings of the 17th annual international conference on ..., 2003 - portal.acm.org

... Methods that **approximate** cache misses for these types of indices have presented in [4 ... histogram at compile-time, we need to compute the **stack distance** for each ...

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[StatCache: a probabilistic approach to efficient and accurate data locality analysis](#) - group of 3 »

E Berg, E Hagersten - Performance Analysis of Systems and Software, 2004 IEEE ..., 2004 - ieeeexplore.ieee.org

... method for peifonning dara-loca/ili' **analysis** on realistic ... For example the **stack distance** of reference 6 is ... The solution is to **approximate** the varying miss ...

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[Calculating stack distances efficiently](#) - group of 5 »

G Almási, C Caşcaval, DA Padua - Proceedings of the workshop on Memory system performance, 2003 - portal.acm.org

... misses in fully associative caches, and to **approximate** them in ... and the element is pushed on top of the **stack**. 4.1.1 **Analysis** For each reference in the trace ...

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[Method and structure for evaluating and enhancing the performance of cache memory systems](#) - group of 3 »

MA Salsburg - US Patent 5,452,440, 1995 - Google Patents